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FAX:	1 (703)	746-9195					
DATE:	11-29-	2004		TIME:	4:00 PM		
		Without Confirm With Confirmatio					
Application Filed:	No.	10/733,884 December 3, 200	3		,		
		document and all des. Fax No. (703) _746-		low are being	g transmitted via facsimile to 29, 2004		
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Under the Phoenwork Red	uction Act of 1995, no berso	Application Number	10/733.	lon unless It disolm	EPARTMENT OF COMMERS of a valid OMB control numb
TRANSM	NITTA1	Filling Date			
FOR		First Named Inventor		12-03-2003	
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(to be used for all correspor	ndence after initial filing)	Art Unit	2823		
		Examiner Name	Dang, Tr	Dang, Trung	
Yestel Number of Pages in Tr	ols Submission 5	Attorney Docket Number	OF03P2	12/ÜS	
	ENG	CLOSURES (Check all t	het apply)		
	ciaration(s) cequest ent Request ine Statement ority Rem	Drawing(s) Licensing-related Papers Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence As Terminal Disclaimer Request for Refund CD, Number of CD(s)	idress - 1	After Allowance communication Technology Center (TC) Appeal Communication to Be of Appeals and Interferences Appeal Communication to TC (Appeal Notice, Brief, Reply Br	
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yped or printed name	Jennie Heaton				
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process) an application. Confidentiality is governed by 33 U.S.C. 122 and 37 CPT, 1.4. This collection is estimated to 2 nature to complete, including training, preparing, and submitting the completed application form to the USPTO. Time will very depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burder, should be sent to the Chair information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450, DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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November 29, 2004

Office of Initial Patent Examination / Filing Receipt Corrections Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

RE: U.S. Patent Application No. 10/733,884

Title: "Method of Manufacturing a

Semiconductor Device"

Filed: 12-03-2003

Atty Docket No.: OF03P212/US Applicants: Jac Suk LEE et al.

To Whom It May Concern:

In the above identified application, the first named inventor on the Filing Receipt is misspelled. As shown on the attached Filing Receipt copy, 'Ja' should have been spelled 'Jae'. A corrected Filing Receipt for the above identified application is respectfully requested.

Sincerely.

Andrew D. Fortney, Ph.D.

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ADF:jkh

Page 1 of 2



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APPL NO.	Filing or 371 (a) Date	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS	
10/733,884	12/03/2003	2812	770	OF03P212/US	4	5	1	

CONFIRMATION NO. 1568

36872 THE LAW OFFICES OF ANDREW D. FORTNEY, PH.D., P.C. 7257 N. MAPLE AVENUE BLDG. D. 3107 FRESNO, CA 93720

Date Mailed: 03/18/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt Incorporating the requested corrections (if appropriate).

Applicant(s)

Jae

Ja Suk Lee, Icheon-SI, KOREA, REPUBLIC OF: Ji A. Kim, Seoul, KOREA, REPUBLIC OF:

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 10-2002-0080224 12/16/2002

If Required, Foreign Filing License Granted: 03/17/2004

Projected Publication Date: 06/24/2004

Non-Publication Request: No

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Early Publication Request No

MAR 2 2 2004

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Title

Method of manufacturing a semiconductor device

Preliminary Class

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